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Application Number	TBA 09/970,388
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First Named Inventor	Lee, Seong-Hoon
Group Art Unit	2824
Examiner Name	C. DINH
Attorney Docket Number	00939H-079810US



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Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SJ	A	WP 24.5 A250Mb/s/pin 1Gb Double Data Rate SDRAM with a Bi-Directional Delay and an Inter-Bank Shared Redundancy Scheme, 1998 IEEE International Solid-State Circuits Conference	
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